A LOW-VOLTAGE LOW *Q* SECOND ORDER BANDPASS FILTER REALIZATION THROUGH FGMOS BASED CCII

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Key words: Low-voltage circuits, Current conveyors, Active filters

ABSTRACT

A second order low Q, low voltage, bandpass filter based on a single CCII and four grounded passive components imparting the ease in chip implementation is presented. The proposed filter structure can easily be cascaded to get enhanced Q. Theoretical results have been validated by PSpice simulations for $0.5\mu m$ technology at ± 0.75 V.

I. INTRODUCTION

The low voltage (LV) analog circuits are well established due to the mobility in applications and the filters form an integral part of any LV modern communication and instrumentation equipment [1-3]. Current conveyor type II (CCII) has been used in almost all linear and non-linear current mode applications exhibiting performance superiority over the conventional op amps [4]. LV CCII can be designed by employing LV circuit design techniques [1-3]. Floating gate MOSFET (FGMOS) is one such LV design technique where threshold voltage can be tailored without device scaling [5-7].

There are many CCII based filter structures but most of them use either number of CCIIs or number of passive components [8-15]. Some structures, however, use floating components which complicate their implementation in chip form [15]. In this paper, we present a low Q bandpass filter realized using a low voltage CCII+. Low Q filters are more or less insensitive to component variations [16]. The Q of the bandpass filter can be enhanced by cascading identical stages. The operation of these circuits has been verified by using PSpice in 0.5 μ m technology at supply voltage of \pm 0.75 V.

II. CIRCUIT DESCRIPTION

The circuit for the voltage-mode bandpass filter is shown in Fig. 1.

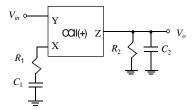


Fig.1 Voltage-mode bandpass filter The transfer function is given as

$$\frac{V_o}{V_{in}} = \frac{sC_1R_2}{s^2C_1C_2R_1R_2 + s(C_1R_1 + C_2R_2) + 1}$$
(1)

which gives Center frequency $\omega_0 = \frac{1}{\sqrt{C_1 C_2 R_1 R_2}}$, Quality

factor
$$Q = \frac{\sqrt{C_1 C_2 R_1 R_2}}{C_1 R_1 + C_2 R_2}$$
 and Passband gain $k = \frac{C_1 R_2}{C_1 R_1 + C_2 R_2}$

This circuit when realized with current controlled current conveyor (CCCCII+), intrinsic resistance R_X and R_1 come in series. Since R_X depends on the biasing current flowing through CCCCII+, the structure can provide tunability or Q factor adjustment.

III. FILTER DESIGN

The band-pass filter can be designed to yield different results depending upon the relationship between various components as shown:

A: For equal valued components as $R_1 = R_2 = R$ & $C_1 = C_2 = C$, Eqn. (1) gives $f_0 = \frac{1}{2\pi RC}$, Q = 0.5 and k = 0.5.

B: For
$$R_1 = R$$
, $R_2 = 2R \& C_1 = 2C C_2 = C$, we get $f_0 =$

$$\frac{1}{4\pi RC}$$
, $Q = 0.5$ and $k = 1$

C: For
$$R_1 = R$$
, $R_2 = 4R \& C_1 = 4C C_2 = C$, we have $f_0 = \frac{1}{8\pi RC}$, $Q = 0.5$ and $k = 2$.

D: For $R_1 = R$, $R_2 = 6R \& C_1 = 6C C_2 = C$, we have $f_{0} = 1$ Q = 0.5 and k = 3.

$$12\pi RC$$

Hence, pass band gain can be increased at the expense of center frequency while retaining the quality factor.

Design II

Now for $C_1 = C_2 = C$ and $R_2 = KR_1$, we have the transfer function as

$$\frac{V_o}{V_{in}} = \frac{sKCR}{s^2 KC^2 R^2 + s(K+1)CR + 1}$$
(2)
which gives $\omega_0 = \frac{1}{CR\sqrt{K}}, Q = \frac{\sqrt{K}}{1+K}$ and $k = \frac{K}{1+K}$.

Here, Q can be varied between 0.5 and lower while resonant frequency decreases and passband gain increases with increase in the value of resistance scaling factor K.

IV. SIMULATION RESULTS

CCII (+) used for realizing bandpass filter is taken from reference [3] wherein FGMOS current mirrors [6] have been used. It has been simulated using PSpice for 0.5µm technology with supply voltage of \pm 0.75 V. Simulation results show that it offers an input resistance of 2.53 Ω at port X, 10²⁰ Ω at port Y and output resistance of 119.8 M Ω at port Z. The power consumed by the circuit is 1.62 mW. Current and voltage transfer ratios are almost unity with an error less than \pm 0.2 %. The bandwidth for both current and voltage transfer has been found to be 100 MHz.

The filter circuit for design I is simulated by choosing C = 500 pF and R = 0.5 k Ω . The simulated magnitude and phase responses are shown in Figs 2(a) & 2(b) respectively. The simulated filter response for design II for different values of *K* is shown in Fig. 3(a) & 3(b). The simulated results agree well with the results of theoretical analysis (Tables 1 & 2). The effect of cascading on the enhancement of *Q* has also been observed. The cascaded filters are simulated for identical values of components and it has been found that *Q* increases to 0.732 for two-stage filter and to 0.914 for three-stage filter from 0.495 for a single-stage filter.

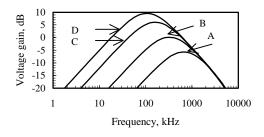


Fig. 2(a) Magnitude response of design I

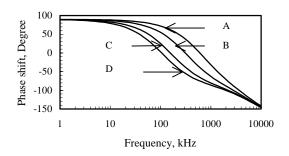


Fig. 2(b) Phase response of design I

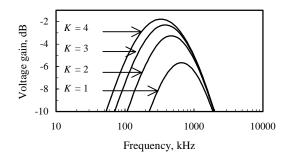


Fig. 3(a) Magnitude response of design II

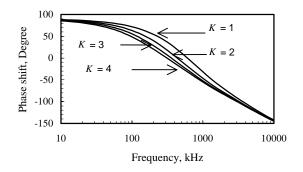


Fig. 3(b) Phase response of design II

Table1-Performance of Filter design I (Q = 0.5)

Filter parameters		Type of filter design				
		А	В	C	D	
<i>f</i> ₀ (kHz)	Cal.	636	318	159	106	
	Simu.	631	316	159	100	
<i>k</i> (dB)	Cal.	-6	0	6.02	9.54	
	Simu.	-5.66	0.15	6.08	9.55	

Filter		Type of filter design				
parameters		<i>K</i> =1	<i>K</i> =2	<i>K</i> =3	<i>K</i> =4	
f ₀ (kHz)	Cal.	636	449.79	367.2	318	
	Simu.	631	501	398	316	
<i>k</i> (dB)	Cal.	-6	-3.48	-2.50	-1.94	
	Simu.	-5.66	-3.28	-2.31	-1.79	
Q	Cal.	0.5	0.471	0.433	0.40	
	Simu.	0.495	0.464	0.423	0.389	

Table 2-Performance of Filter design II

V. CURRENT-MODE BANDPASS FILTER

The filter structure can be modified to yield the currentmode bandpass filter as shown in Fig.4. The analysis shows that the characteristics of this structure are similar to those derived for the voltage mode structure

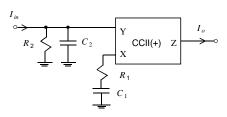


Fig.4 Current-mode bandpass filter

When this circuit has been simulated for the same design values (as have been used in voltage-mode bandpass filter) the resultant characteristics resembles to the voltage mode structure. However, in this case input impedance decreases to the maximum R_2 , which further decreases as the input signal frequency increases.

VI. CONCLUSION

The low Q, CCII+ based voltage-mode bandpass filter uses only four grounded passive components, which are desirable for integration on a chip. The low Q filter presented is useful for cascading active filters. A currentmode version of bandpass filter also has similar characteristics. When the CCII+ is replaced by a CCCCII+, electronic tunability can be achieved. All these circuit structures operate with at ± 0.75 V.

REFERENCES

- S. Yan & E. Sanchez-Sinencio, Low Voltage Analog Circuit Design Techniques: A Tutorial, IEICE Trans. Fundamentals, Vol. E83-A, 2000.
- S. S. Rajput & S. S. Jamuar, Low voltage analog circuit design techniques, IEEE Circuits and Systems Magazine, Vol. 2, No.1, pp.24-42, 2002.
- S.S. Rajput, Low voltage current mode analog circuit structures and their applications, Ph.D. Thesis, Indian Institute of Technology, Delhi, 2002.

- A. S. Sedra & K. C. Smith, A second generation current conveyor and its applications, IEEE Trans. Circuits Theory, pp. 132-133, 1970.
- J. Ramirez-Angulo, S. C. Choi, & G. G. Altamirano, Low voltage circuits building blocks using multiple input floating gate transistors, IEEE Trans. Circuits Syst.-I, Vol. 42, No. 11, pp. 971-974, 1995.
- Susheel Sharma, S.S. Rajput, L.K. Magotra & S.S. Jamuar, FGMOS based wide range low voltage current mirror and its applications, Proceedings of 2002 IEEE Asia-Pacific Conference on Circuits and Systems, pp. 331-334, Bali, Indonesia, 2002.
- Susheel Sharma, S.S. Rajput, L.K. Magotra & S.S. Jamuar, FGMOS Current Mirror: Behaviour and Bandwidth Enhancement, Accepted in Analog Integrated Circuits and Signal Processing, Springer.
- 8. M. Soliman, Two novel RC canonic band pass network using current conveyor, Int. J. Electronics, Vol. 42, No. 1, pp. 49-54, 1977.
- 9. R. Nandi, Active inductance using current conveyor and their application in a simple bandpass filter realization, Electronics Letters, Vol. 14, No. 14, pp. 373-375, 1978.
- C. P. Chong & K. C. Smith, Biquadratic filter sections employing a single current conveyor, Electronics Letters, Vol.22, No.22, pp. 1662-1663, 1986.
- V. K. Singh & R. Senani, New multifunction active filter configuration employing current conveyors, Electronics Letters, Vol. 26, No.21, pp.1814-1815, 1990.
- M. Higashimura, Realisation of voltage-mode biquads using CCIIs, Electronics Letters, Vol. 27, No. 15 pp.1345-1346, 1991.
- C. M. Chang & M. S. Lee, Universal voltagemode filter with three inputs and one output using three current conveyors and one voltage follower, Electronics Letters, Vol.30, No. 25, pp. 2112-2113, 1994.
- J. W. Horng, J. R. Lay, Chia Wen Chang & Maw – Huei Lee, High input impedance voltagemode multifunction filters using plus type CCIIs, Electronics Letters, Vol. 33, No. 6, pp. 472-473, 1997.
- Jiun- Wei Horng, High input impedance voltagemode universal biquadratic filter using three plus type CCIIs, IEEE Trans. on Circuit & Sys., Vol. 48, No. 10, pp. 996-997, 2001.
- 16. B. Metin & O Cicekoglu, Novel minimum component second order low *Q* all-pass and notch filters with Inverting second generation current conveyor, IJCI Proceedings of International Conference on Signal Processing, ISSN 1304-2386, Vol.1, No.2, 2003.