# EFFECT OF COLLECTOR DOPING ON INP BASED DOUBLE HETEROJUNCTION BIPOLAR TRANSISTOR

Serkan Topaloglu, Jörn Driesen, Werner Prost, F.J.Tegude e-mail: topaloglu@hlt.uni-duisburg.de Solid State Electronics Department Faculty of Engineering University Duisburg - Essen, 47057, Duisburg, Germany

Key words: collector doping, Kirk effect, DHBT, high current

## ABSTRACT

In this study, high current effects on DHBT performance are investigated. Three samples with different collector doping are experimentally realized. DC and RF measurements have been done to see the influence of collector doping and related Kirk effect on HBT performance.

### I. INTRODUCTION

Especially by the advances in optical communication systems, InP-based Heterojunction Bipolar Transistors (HBTs) were intensively investigated in recent years. One of the main advantages of this material is the compatibility with the optoelectronic devices, which operates at the  $1.3\mu m$  and  $1.55\mu m$  communication wavelengths, where the optical attenuation is at its minimum. Moreover, since HBTs have high transconductance, high current density and excellent RF behaviour, they are also attractive for high-speed electronic applications.

The figures of merit for high frequency performance are the cut-off frequency  $f_T$  and the maximum frequency of oscillation  $f_{max}$  [1].

$$\frac{1}{2\pi \cdot f_T} = \tau_{base} + \tau_{collector} + C_{je} \frac{kT}{qI_E} + C_{bc} \left(\frac{kT}{qI_E} + R_{ex} + R_{coll}\right) \quad (1)$$

$$f_{\max} \cong \sqrt{\frac{f_T}{8\pi \cdot R_{BB}C_{BC}}}$$
(2)

As it is depicted in equation (1), the cut-off frequency is limited by the transition times, which are mostly influenced by the vertical dimension of the HBT structure. In contrary to this, maximum oscillation frequency is mostly affected by the parasitic capacitances and resistances, which can be controlled by lateral scaling of the devices.

Briefly,  $f_T$  can be improved by epitaxy and processing and layout can improve fmax.

To achieve higher  $f_{max}$  values, several techniques have been introduced in the literature. These are base undercut, transferred substrate HBTs, buried collector HBTs, in which the parasitic base collector ( $C_{BC}$ ) can be reduced [2-7].

By decreasing the base and collector thickness, the transit frequency  $f_T$  can be improved but at the same time  $f_{max}$  may be affected adversely.

In this work, we have investigated the effect of collector doping on improvement of  $f_T$ . When the collector current increases ( $I_C > q N_{dC} v_{sat}$ ), the electron density entering the base collector depletion region exceeds the doping level and this changes the electric field profile in the junction. This will cause increase of hole injection from base to collector and so increasing the base width. Since there is a larger distance for carriers to penetrate, this will degrade the current gain and also increase the base transit time. Doping the collector can minimize this situation. The details about Kirk Effect have been explained in [8,9].

#### **II. EXPERIMENTAL**

The epitaxial layer structure used for the device fabrication is listed in Table 1.

Table 1. Layer structure of the processed DHBTs		
n++ InGaAs	E-Cap	50nm
n+ InP	E-contact	50nm
n InP	Emitter	50nm
p InGaAs	Base	50nm
InGaAs	Spacer	30nm
InGaAsP	Quaternary layer	10nm
InGaAsP	Quaternary layer	20nm
InP	Collector	100nm
n+ InP	<b>Collector contact</b>	100nm
n++ InGaAs	Sub-collector	200nm
InP	Buffer	50nm
SI InP Substrate		

The structure was grown by LP-MOVPE (Low Pressure-Metal Organic Vapour Phase Epitaxy) (Aixtron 200) on (001)  $\pm$  0.5° oriented semi insulating (001) InP (Fe) substrate. Three samples were used for this investigation. Sample A, Sample B and Sample C have the same layer structure depicted in table 1, but the collector doping differs. The collector doping is nid (A) (non-intentionally doped),  $5 \times 10^{16}$  cm<sup>-3</sup> (B),  $5 \times 10^{17}$  cm<sup>-3</sup> (C), respectively. These samples are processed in parallel, to eliminate any deviation that may occur by environmental effect during processing.

Device fabrication is carried out by conventional wet chemical etching based on phosphoric acid  $(H_3PO_4)$  for InGaAs and InGaAsP layers, and hydrochloric acid for InP containing layers. The emitter, base and collector layers are defined by optical lithography. The Ti/Pt/Au contact metal system is used for emitter and collector contacts. The self-aligned base metallization is deposited as Pt/Ti/Pt/Au. Traditionally, air bridges are used for the connections to the measurement pads. SEM (Scanning Electron microscope) picture of one of the realized HBTs is shown in Figure 1.



Figure 1. SEM picture of HBT with nominal  $A_E = 2x10 \mu m^2$ 

**III. RESULTS and DISCUSSION** The DC characteristics of the DHBTs were measured by an HP4515B parameter analyzer.



Figure 2. Common Emitter Output Characteristics for Sample A, B and C

In figure 2 common emitter output characteristic for sample A, B and C are shown. The DC current gain is  $\sim$ 70 for all three samples at I<sub>B</sub>=200µA.



Figure 3. Gummel Plot for Sample A



Figure 4. Gummel Plot for Sample B



Figure 5. Gummel Plot for Sample C

In Figure 3,4 and 5, high current regions are marked with dashed circle. In these circles, Kirk effect, the sudden

increase in the base current  $(I_B)$  and a slight decrease in the collector current  $(I_C)$  can be seen.

From the Gummel plots, it can be seen that by the increased doping of collector, the current density  $(J_{Kirk})$  where the high current effects appear, has increased, too [10].

On the other hand, the breakdown voltage has decreased by increasing collector doping. ( $BV_{CEsampleA}$ = 5.5V,  $BV_{CEsampleB}$ = 4.5V,  $BV_{CEsampleC}$ = 3V)

After having these results from DC measurements, high frequency measurements using an HP8510C network analyser, have been done. For these measurements, again the same devices ( $A_E=2x10\mu m^2$ ) are used. Here, Sample A, B and C have shown transit frequency ( $f_T$ ) of 100GHz, 120GHz and 165GHz, respectively.



0,00E+000 1,00E+017 2,00E+017 3,00E+017 4,00E+017 5,00E+017 6,00E+017 N<sub>c</sub> (cm<sup>3</sup>)

Figure 7. Compromise for collector doping to  $J_{Kirk}$  and breakdown voltage

The effect of collector doping on the  $J_{Kirk}$  and collector emitter breakdown voltage can be seen at the same time. These results are also crosschecked with some calculation using the equation (3).

$$J_{Kirk} = (1 + \frac{V_{CB} + \phi_{CB}}{V_2 + \phi_{CB}}) q N_C v_{sat}$$
(3)

where;

 $\phi_{CB}$  : Base collector junction potential

 $V_2$  : applied base-collector bias that totally depletes collector layer when  $J_C\!\!=\!\!0$ 

N<sub>C</sub> : Collector doping

 $v_{sat}$  : Saturation velocity

Calculated Kirk current densities (J<sub>Kirk</sub>) are as follows; J<sub>Kirk,A</sub>=0.9mA/ $\mu$ m<sup>2</sup>,J<sub>Kirk,B</sub>=2.5mA/ $\mu$ m<sup>2</sup>, J<sub>Kirk,C</sub>=6.5mA/ $\mu$ m<sup>2</sup>. These values fit really nice with the values measured.

## **IV. CONCLUSION**

Kirk effect has been experimentally observed on DHBT performance. To see the effect, DC measurements have been done and by the help of Gummel Plot the point where the Kirk effect occurs,  $J_{Kirk}$ , (the point where sudden increase in base current occurs) has been identified. It has been concluded that doping the collector can reduce this effect. This will lead to better RF performance, but a compromise is necessary, because when the collector is doped, breakdown voltage decreases. All these experimental results are compared with the theoretical calculations and good fitting has been achieved.

#### REFERENCES

1. Rodwell, M.J.W.; *et. al*, Submicron scaling of HBTs Electron Devices, IEEE Transactions on, Volume 48, Issue 11, Nov. 2001 Page(s):2606 - 2624

2. Lee, S.; Urteaga, M.; Wei, Y.; Kim, Y.; Dahlstrom, M.; Krishnan, S.; Rodwell, M., Ultra high  $f_{max}$  InP/InGaAs/InP transferred substrate DHBTs, Device Research Conference, 2002. 60th DRC. Conference Digest

24-26 June 2002 Page(s):107 - 108

3. Lee, Q.; Agarwal, B.; Mensa, D.; Pullela, R.; Guthrie, J.; Samoska, L.; Rodwell, M.J.W.;A>400 GHz  $f_{max}$  transferred-substrate heterojunction bipolar transistor IC technology, Electron Device Letters, IEEE Volume 19, Issue 3, March 1998 Page(s):77 – 79

4. Lee, S.; Kim, H.J.; Urteaga, M.; Krishnan, S.; Wei, Y.; Dahlstrom, M.; Rodwell, M.;Transferred-substrate InP/InGaAs/InP double heterojunction bipolar transistors with  $f_{max}$ =425 GHz, Electronics Letters, Volume 37, Issue 17, 16 Aug. 2001 Page(s):1096 - 1098

5. Sato, H.; Vlcek, J.C.; Fonstad, C.G.; Meskoob, B.; Prasad, S.;InGaAs/InAlAs/InP collector-up microwave heterojunction bipolar transistors, Electron Device Letters, IEEE,Volume 11, Issue 10, Oct. 1990 Page(s):457 - 459

6. Kyungho Lee, Daekyu Yu, Minchul Chung, Jongchan Kang, andBumman Kim, New Collector Undercut Technique Using a SiN Sidewall for Low Base Contact

Resistance in InP/InGaAs SHBTs, IEEE Transactions on Electron Dveices, Vol. 49, No. 6, June 2002 Pages: 1079-1082

7. Arai, T.; Harada, Y.; Yamagami, S.; Miyamoto, Y.; Furuya, K.;  $C_{BC}$  reduction in GaInAs/InP buried metal heterojunction bipolar transistor, Indium Phosphide and Related Materials, 2000. Conference Proceedings. 2000 International Conference on 14-18 May 2000 Page(s):254 - 257

8. C.T. Kirk, A theory of Transistor Cutoff Frequency Fallof at High Current Densities, IRE Transactions on Electron Devices, March 1962, page: 164-174

9. William Liu, Fundamentals of III-V Devices; HBTs, MESFETs, and HFETs/HEMTs, John Wiley &Sons, Inc., page186-196

10. M. Ida, K. Kurishima, N. Watanabe, Ultrahigh-Speed InP/InGaAs DHBTs with very High Current Density, IEICE Trans. Electron., Vol.E86, No.10 October 2003, page: 1923-1928