# COMPARISON OF TWO GENERAL CONTROL METHOD FOR THREEPHASE TO THREE-PHASE AC/AC MATRIX CONVERTERS 

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#### Abstract

This paper presents a complete comparison between two general control method for three-phase to three-phase AC/AC matrix converters. The first algorithm is based on Least Mean Square Errors (LMSE) method and the second algorithm is based on Novel PWM method. In this paper, the methods are compared from many important points, for example THD, the size of input and output filters, the range of amplitude and frequency of converter, the frequency of switches (thyristor, IGBT, Diode, transistor) and etc. The validity of the aspects is confirmed by the simulation results.


## I. INTRODOCTION

The principle of a matrix converter was first proposed in 1980. The matrix converter offers an "all silicon" Solution for AC/AC conversion. By selective firing of semi-conductor devices it is possible to directly convert alternating current (AC) to alternating current without the need for a direct current (DC) link. The matrix converter as three-phase to three-phase direct converter has number of attractive features such as: sinusoidal input/output currents, power factor adjustment capability and instantaneous power conversion. Matrix converter does not require bulk reactive component, it is mainly electronic conversion technique and therefore potentially can be easily integrated delivering saving in weight and size. In matrix converter, the reactive power and real power can be controlled separately for three-phase input and three-phase output, and the energy flow is bidirectional [1], [2].

## II. MATRIX CONVERTER THEORY

## A. The Matrix Converter Definition

The three-phase to three-phase AC/AC matrix converter in a schematic form, is presented in Fig. 1, having as inputs the phase voltages $v_{i 1}, v_{i 2}, v_{i 3}$, and as outputs the phase voltages $v_{o 1}, v_{o 2}, v_{o 3}$. The "matrix components" $S_{11}, S_{12}, \ldots, S_{33}$ represent nine bi-directional switches capable to block voltage in both directions and switch without delays. The presented matrix converter will
connect the three given inputs, with constant amplitude and frequency, through the nine switches to the output terminals in accordance to pre-calculated switching patterns. The obtained outputs three-phase voltage system, have controllable amplitudes and frequency.


Figure 1. Matrix converter topology
The input voltages of voltage of the matrix converter are given by:

$$
\begin{align*}
& v_{i 1}(t)=V_{i} \sin \left(\omega_{i} t\right) \\
& v_{i 2}(t)=V_{i} \sin \left(\omega_{i} t-\frac{2 \pi}{3}\right)  \tag{1}\\
& v_{i 3}(t)=V_{i} \sin \left(\omega_{i} t+\frac{2 \pi}{3}\right)
\end{align*}
$$

Where $V_{i}$ denotes a peak value of input voltages, and $\omega_{i}$ denotes an angular frequency of input voltages.
The matrix converter will be designed and controlled in such a manner that the fundamental of the output voltages are:

$$
\begin{align*}
& v_{o 1}(t)=V_{o} \sin \left(\omega_{o} t\right) \\
& v_{o 2}(t)=V_{o} \sin \left(\omega_{o} t-\frac{2 \pi}{3}\right)  \tag{2}\\
& v_{o 3}(t)=V_{o} \sin \left(\omega_{o} t+\frac{2 \pi}{3}\right)
\end{align*}
$$

Where $V_{o}$ denotes a peak value of desired output voltages, and $\omega_{o}$ denotes an angular frequency of output voltages [3].

## B. The first form of control strategy for three-phase to three-phase AC/AC matrix converter

The three-phase to three-phase AC/AC matrix converter consists of $3 \times 3$ switches. The $3 x 3$ switches give 512 combinations of switching states, which are decreased to only 27 permitted states, if the two basic rules to operate this converter safely are applied:

- For prevent of short-circuit of power supplies, do not connect two different input lines to the same output line (over currents);
- For prevent of open circuit of loads (Load is inductive), do not disconnect the output line circuits (over voltages).
Table (I) shows different operating Modes of matrix converter [3].

TABLE I. The permitted switching states in a three-phase to three-phase matrix converter

| MODE | ON SWITCHES |  | $v_{o 1}$ | $v_{o 2}$ | $v_{o 3}$ |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\mathbf{1}$ | $\mathrm{~S}_{11}$ | $\mathrm{~S}_{22}$ | $\mathrm{~S}_{33}$ | $v_{i 1}$ | $v_{i 2}$ | $v_{i 3}$ |
| $\mathbf{2}$ | $\mathrm{~S}_{12}$ | $\mathrm{~S}_{23}$ | $\mathrm{~S}_{31}$ | $v_{i 3}$ | $v_{i 1}$ | $v_{i 2}$ |
| $\mathbf{3}$ | $\mathrm{~S}_{13}$ | $\mathrm{~S}_{21}$ | $\mathrm{~S}_{32}$ | $v_{i 2}$ | $v_{i 3}$ | $v_{i 1}$ |
| $\mathbf{4}$ | $\mathrm{~S}_{13}$ | $\mathrm{~S}_{22}$ | $\mathrm{~S}_{31}$ | $v_{i 3}$ | $v_{i 2}$ | $v_{i 1}$ |
| $\mathbf{5}$ | $\mathrm{~S}_{11}$ | $\mathrm{~S}_{23}$ | $\mathrm{~S}_{32}$ | $v_{i 1}$ | $v_{i 3}$ | $v_{i 2}$ |
| $\mathbf{6}$ | $\mathrm{~S}_{12}$ | $\mathrm{~S}_{21}$ | $\mathrm{~S}_{33}$ | $v_{i 2}$ | $v_{i 1}$ | $v_{i 3}$ |
| $\mathbf{7}$ | $\mathrm{~S}_{11}$ | $\mathrm{~S}_{22}$ | $\mathrm{~S}_{23}$ | $v_{i 1}$ | $v_{i 2}$ | $v_{i 2}$ |
| $\mathbf{8}$ | $\mathrm{~S}_{21}$ | $\mathrm{~S}_{32}$ | $\mathrm{~S}_{33}$ | $v_{i 2}$ | $v_{i 3}$ | $v_{i 3}$ |
| $\mathbf{9}$ | $\mathrm{~S}_{12}$ | $\mathrm{~S}_{13}$ | $\mathrm{~S}_{31}$ | $v_{i 3}$ | $v_{i 1}$ | $v_{i 1}$ |
| $\mathbf{1 0}$ | $\mathrm{~S}_{12}$ | $\mathrm{~S}_{13}$ | $\mathrm{~S}_{21}$ | $v_{i 2}$ | $v_{i 1}$ | $v_{i 1}$ |
| $\mathbf{1 1}$ | $\mathrm{~S}_{22}$ | $\mathrm{~S}_{23}$ | $\mathrm{~S}_{31}$ | $v_{i 3}$ | $v_{i 2}$ | $v_{i 2}$ |
| $\mathbf{1 2}$ | $\mathrm{~S}_{11}$ | $\mathrm{~S}_{32}$ | $\mathrm{~S}_{33}$ | $v_{i 1}$ | $v_{i 3}$ | $v_{i 3}$ |
| $\mathbf{1 3}$ | $\mathrm{~S}_{12}$ | $\mathrm{~S}_{21}$ | $\mathrm{~S}_{23}$ | $v_{i 2}$ | $v_{i 1}$ | $v_{i 2}$ |
| $\mathbf{1 4}$ | $\mathrm{~S}_{22}$ | $\mathrm{~S}_{31}$ | $\mathrm{~S}_{33}$ | $v_{i 3}$ | $v_{i 2}$ | $v_{i 3}$ |
| $\mathbf{1 5}$ | $\mathrm{~S}_{11}$ | $\mathrm{~S}_{13}$ | $\mathrm{~S}_{32}$ | $v_{i 1}$ | $v_{i 3}$ | $v_{i 1}$ |
| $\mathbf{1 6}$ | $\mathrm{~S}_{11}$ | $\mathrm{~S}_{13}$ | $\mathrm{~S}_{22}$ | $v_{i 1}$ | $v_{i 2}$ | $v_{i 1}$ |
| $\mathbf{1 7}$ | $\mathrm{~S}_{21}$ | $\mathrm{~S}_{23}$ | $\mathrm{~S}_{32}$ | $v_{i 2}$ | $v_{i 3}$ | $v_{i 2}$ |
| $\mathbf{1 8}$ | $\mathrm{~S}_{12}$ | $\mathrm{~S}_{31}$ | $\mathrm{~S}_{33}$ | $v_{i 3}$ | $v_{i 1}$ | $v_{i 3}$ |
| $\mathbf{1 9}$ | $\mathrm{~S}_{13}$ | $\mathrm{~S}_{21}$ | $\mathrm{~S}_{22}$ | $v_{i 2}$ | $v_{i 2}$ | $v_{i 1}$ |
| $\mathbf{2 0}$ | $\mathrm{~S}_{23}$ | $\mathrm{~S}_{31}$ | $\mathrm{~S}_{32}$ | $v_{i 3}$ | $v_{i 3}$ | $v_{i 2}$ |
| $\mathbf{2 1}$ | $\mathrm{~S}_{11}$ | $\mathrm{~S}_{12}$ | $\mathrm{~S}_{33}$ | $v_{i 1}$ | $v_{i 1}$ | $v_{i 3}$ |
| $\mathbf{2 2}$ | $\mathrm{~S}_{11}$ | $\mathrm{~S}_{12}$ | $\mathrm{~S}_{23}$ | $v_{i 1}$ | $v_{i 1}$ | $v_{i 2}$ |
| $\mathbf{2 3}$ | $\mathrm{~S}_{21}$ | $\mathrm{~S}_{22}$ | $\mathrm{~S}_{33}$ | $v_{i 2}$ | $v_{i 2}$ | $v_{i 3}$ |
| $\mathbf{2 4}$ | $\mathrm{~S}_{13}$ | $\mathrm{~S}_{31}$ | $\mathrm{~S}_{32}$ | $v_{i 3}$ | $v_{i 3}$ | $v_{i 1}$ |
| $\mathbf{2 5}$ | $\mathrm{~S}_{11}$ | $\mathrm{~S}_{12}$ | $\mathrm{~S}_{13}$ | $v_{i 1}$ | $v_{i 1}$ | $v_{i 1}$ |
| $\mathbf{2 6}$ | $\mathrm{~S}_{21}$ | $\mathrm{~S}_{22}$ | $\mathrm{~S}_{23}$ | $v_{i 2}$ | $v_{i 2}$ | $v_{i 2}$ |
| $\mathbf{2 7}$ | $\mathrm{~S}_{31}$ | $\mathrm{~S}_{32}$ | $\mathrm{~S}_{33}$ | $v_{i 3}$ | $v_{i 3}$ | $v_{i 3}$ |

The first form of control strategy for three-phase to threephase AC/AC matrix converter is based on LMSE method. The difference (Error) between measured and forecasted outputs is $E_{1}$, when $S_{11}, S_{22}, S_{33}$ are closed (Mode 1), $E_{2}$ when $S_{12}, S_{23}, S_{31}$ are closed (Mode 2), $\ldots$ and finally $E_{27}$ when $S_{31}, S_{32}, S_{33}$ are closed (Mode 27). The equations of $E_{1}, E_{2}, \ldots, E_{27}$, are as follow:
$E_{1}(t)=\left[v_{o 1, \text { Forc. }}(t)-v_{i 1}(t)\right]^{2}+\left[v_{o 2, \text { Forc. }}(t)-v_{i 2}(t)\right]^{2}+\left[v_{o 3, \text { Forc. }}-v_{i 3}(t)\right]^{2}$
$E_{2}(t)=\left[v_{o 1, \text { Forc. } .}(t)-v_{i 3}(t)\right]^{2}+\left[v_{o 2, \text { Forc. }}(t)-v_{i 1}(t)\right]^{2}+\left[v_{o 3, \text { Forc. }}-v_{i 2}(t)\right]^{2}$
$E_{27}(t)=\left[v_{o l, \text { For. } .}(t)-v_{i 3}(t)\right]^{2}+\left[v_{o 2, \text { Forc. }}(t)-v_{i 3}(t)\right]^{2}+\left[v_{o 3, \text { Forc. }}-v_{i 3}(t)\right]^{2}$

This method is based on LMSE Method. In another words we have:
$\operatorname{IF}\left(\min \left(E_{1}, E_{2}, \ldots, E_{27}\right)=E_{1}\right) \quad$ THEN $S_{11} S_{22} S_{33}=O N$
$\operatorname{IF}\left(\min \left(E_{1}, E_{2}, \ldots, E_{27}\right)=E_{2}\right) \quad$ THEN $S_{12} S_{23} \quad S_{31}=O N$
$\operatorname{IF}\left(\min \left(E_{1}, E_{2}, \ldots, E_{27}\right)=E_{27}\right) \quad$ THEN $S_{31} \quad S_{32} \quad S_{33}=O N$
The relations between the input currents and the output currents are given by:

$$
\left.\begin{array}{l}
i_{i 1}(t)=\left\{\begin{array}{l}
i_{o 1}(t) \\
i_{o 2}(t) \\
\cdot \\
\cdot \\
0
\end{array} \begin{array}{l}
\text { MODE 1 }
\end{array}\right. \\
i_{i 2}(t)=\left\{\begin{array}{l}
\text { MODE 27 }
\end{array}\right. \\
i_{o 2}(t) \\
i_{o 3}(t) \\
\cdot \\
\cdot \\
0
\end{array} \begin{array}{l}
\text { MODE 1 }
\end{array}\right\}
$$

## C. The second form of control strategy for three-phase to three-phase AC/AC matrix converter

The second form of control strategy for three-phase to single phase AC/AC matrix converter is based on the novel PWM method. This method is an alternative PWM strategy for matrix converters. In this strategy, the sampling time $T_{s}$ will be divided in to 3-time intervals $t_{1}, t_{2}$ and $t_{3}$ (Fig. 2) [4], [5].


Figure 2: The sampling time, $T_{s}$
During any $\mathrm{n}^{\text {th }}$ sampling time $T_{s}$ related with time intervals $t_{1}, t_{2}$ and $t_{3}$ as follows:

$$
\begin{equation*}
T_{s}=t_{1}^{n}+t_{2}^{n}+t_{3}^{n} \tag{4}
\end{equation*}
$$

In this approach, we form the switches matrix, $S$ (eqn. 5).

$$
S=\left[\begin{array}{lll}
S_{11} & S_{12} & S_{13}  \tag{5}\\
S_{21} & S_{22} & S_{23} \\
S_{31} & S_{32} & S_{33}
\end{array}\right]
$$

In switching approach, at $t_{1}$ time interval, the switches in the major-diagonal will be turned on, and the remaining switches will be off, at $t_{2}$ time interval, the switches in the minor-diagonal (next to major-diagonal) will be turned on and the remaining switches will be off, and this procedure continues for $t_{3}$ time interval. Therefore, the average output voltages can be described as eqn. (6).

$$
\begin{align*}
& v_{o 1}(t)=\frac{1}{T_{s}}\left\{v_{i 1} t_{1}+v_{i 3} t_{2}+v_{i 2} t_{3}\right\} \\
& v_{o 2}(t)=\frac{1}{T_{s}}\left\{v_{i 2} t_{1}+v_{i 1} t_{2}+v_{i 3} t_{3}\right\}  \tag{6}\\
& v_{o 3}(t)=\frac{1}{T_{s}}\left\{v_{i 3} t_{1}+v_{i 2} t_{2}+v_{i 1} t_{3}\right\}
\end{align*}
$$

We see that the combination of input voltages at time intervals $t_{1}, t_{2}$ and $t_{3}$ can produce a suitable set of output voltages. For example, $v_{i 1}(t)$ only for time interval $t_{1}$, $v_{i 2}(t)$ only for time interval $t_{2}$ and $v_{i 3}(t)$ only for time interval $t_{3}$ combined together for establishing $v_{o 1}(t)$. Based on the above equations, the switching pattern $t_{1}, t_{2}$ and $t_{3}$. It is obvious that during $\mathrm{n}^{\text {th }}$ sampling time $T_{s}$, the time intervals $t_{1}, t_{2}$ and $t_{3}$, must justify the eqn. (4) and also the following equations:

$$
\begin{equation*}
0 \leq t_{i}^{n} \leq T_{s} \quad i=1,2,3 \tag{7}
\end{equation*}
$$

In the matrix form, the eqn. (6) can be written as follows:

$$
\left[\begin{array}{l}
v_{o 1}(t)  \tag{8}\\
v_{o 2}(t) \\
v_{o 3}(t)
\end{array}\right]=\frac{1}{T_{s}}\left[\begin{array}{lll}
v_{i 1} & v_{i 3} & v_{i 2} \\
v_{i 2} & v_{i 1} & v_{i 3} \\
v_{i 3} & v_{i 2} & v_{i 1}
\end{array}\right]\left[\begin{array}{l}
t_{1} \\
t_{2} \\
t_{3}
\end{array}\right]
$$

or simply:

$$
\left[V_{o}(t)\right]=\frac{1}{T_{s}}\left[V_{i}(t)\right]\left[\begin{array}{l}
t_{1}  \tag{9}\\
t_{2} \\
t_{3}
\end{array}\right]
$$

An unique solution of (8) exists when $\operatorname{det}\left[V_{i}(t)\right] \neq 0$. Because of:

$$
\begin{equation*}
\operatorname{det}\left[V_{i}(t)\right]=\left(v_{i 1}^{3}+v_{i 2}^{3}+v_{i 3}^{3}\right)-3 v_{i 1} v_{i 2} v_{i 3}=0 \tag{10}
\end{equation*}
$$

Therefore infinite number of solution for $t_{1}, t_{2}$ and $t_{3}$ existed. To solve this problem, we assume one of times, $t_{1}, t_{2}$ and $t_{3}$ and calculate others, here we assume:

$$
\begin{equation*}
t_{1}=T_{s}-t_{2}-t_{3} \tag{11}
\end{equation*}
$$

From eqns. (1), (2), (8) and (11) we have:

$$
\begin{align*}
& \frac{t_{1}}{T_{s}}=\frac{1}{3}+\frac{2}{3} \frac{V_{o}}{V_{i}} \cos \left[\left(\omega_{o}-\omega_{i}\right) t\right] \\
& \frac{t_{2}}{T_{s}}=\frac{1}{3}+\frac{2}{3} \frac{V_{o}}{V_{i}} \cos \left[\left(\omega_{o}-\omega_{i}\right) t-\frac{2 \pi}{3}\right]  \tag{12}\\
& \frac{t_{3}}{T_{s}}=\frac{1}{3}+\frac{2}{3} \frac{V_{o}}{V_{i}} \cos \left[\left(\omega_{o}-\omega_{i}\right) t+\frac{2 \pi}{3}\right]
\end{align*}
$$

For justify the eqn. (7) we must be have:

$$
\begin{equation*}
\frac{V_{o}}{V_{i}} \leq \frac{1}{2} \tag{13}
\end{equation*}
$$

The relations between the input currents and the output currents are given by:

$$
\begin{align*}
& i_{i 1}(t)= \begin{cases}i_{o 1}(t) & \text { time int erval } t_{1} \\
i_{o 2}(t) \\
i_{o 3}(t) & \text { time int erval } t_{2} \\
\text { time int erval } t_{3}\end{cases} \\
& i_{i 2}(t)= \begin{cases}i_{o 2}(t) & \text { time int erval } t_{1} \\
i_{o 3}(t) & \text { time int erval } t_{2} \\
i_{o 1}(t) & \text { time int erval } t_{3}\end{cases}  \tag{14}\\
& i_{i 3}(t)= \begin{cases}i_{o 3}(t) & \text { time int erval } t_{1} \\
i_{o 1}(t) & \text { time int erval } t_{2} \\
i_{o 2}(t) & \text { time int erval } t_{3}\end{cases}
\end{align*}
$$

## III. SIMULATION RESULTS

The Simulink Matlab and PSpic soft wares are used for simulations. According to the operation principle, the input and output current and voltage waveforms may be determined by the digital simulation method. Ideal switches constitute the matrix converter. The output terminals loaded by a three-phase $R-L$ load $\left(R=20^{\Omega}, L=40^{m H}\right)$. In all matrix converters, the parameters of input voltage source are $V_{i}=220^{V}$ and $f_{i}=50^{H Z}$. The models are simulated by two-control
method. The relations between the output voltages and the output currents for the three-phase load $(R-L)$ are given by:

$$
\begin{align*}
& v_{o 1}(t)=R i_{o 1}(t)+L \frac{d i_{o 1}(t)}{d t} \\
& v_{o 2}(t)=R i_{o 2}(t)+L \frac{d i_{o 2}(t)}{d t}  \tag{15}\\
& v_{o 3}(t)=R i_{o 3}(t)+L \frac{d i_{o 3}(t)}{d t}
\end{align*}
$$

From eqns. (2) and (15), the fundamentals output currents are given by:

$$
\begin{align*}
& i_{o 1}(t)=\frac{V_{o}}{\sqrt{\left(L \omega_{o}\right)^{2}+R^{2}}} \sin \left(\omega_{o} t-\tan ^{-1}\left(\frac{R}{L}\right)\right) \\
& i_{o 2}(t)=\frac{V_{o}}{\sqrt{\left(L \omega_{o}\right)^{2}+R^{2}}} \sin \left(\omega_{o} t-\frac{2 \pi}{3}-\tan ^{-1}\left(\frac{R}{L}\right)\right)  \tag{16}\\
& i_{o 3}(t)=\frac{V_{o}}{\sqrt{\left(L \omega_{o}\right)^{2}+R^{2}}} \sin \left(\omega_{o} t+\frac{2 \pi}{3}-\tan ^{-1}\left(\frac{R}{L}\right)\right)
\end{align*}
$$

## A) Simulation results for first control method

Figs. 3-6 show the simulation results for three-phase to three-phase AC/AC matrix converter by LMSE Method. The matrix converter controlled in such a way, that the peak value of the fundamental output voltages are $V_{o}=210^{V}$ and $f_{o}=100^{H Z}$.


Figure 3: Waveforms of output voltages and their FFTs


Figure 4: Waveforms of output currents and their FFTs


Figure 5: Waveforms of input currents and their FFTs


Figure 6: Waveforms of the switches commands

## B) Simulation results for second control method

The matrix converter controlled in such a way, that the peak value of the fundamental output voltages are $V_{o}=100^{V}$ and $f_{o}=100^{H Z}$. In this case, the converter operated at a switching frequency of $2^{\mathrm{KHZ}}$. Figs. 7-10 show the simulation results for three-phase to three-phase AC/AC matrix converter by novel PWM method.


Figure 7: Waveforms of output voltages and their FFTs


Figure 8: Waveforms of output currents and their FFTs


Figure 9: Waveforms of the input currents and their FFTs


Figure 10: Waveforms of the switches commands
Table (II) shows THD of the output voltages, output and input currents for both of method.

TABLE II. The THD for both of methods

|  | LMSE METHOD | NOVEL PWM METHOD |
| :---: | :---: | :---: |
| $\boldsymbol{v}_{o}$ | $\mathbf{4 0 . 8 0 \%}$ | $\mathbf{1 8 7 . 9 6 \%}$ |
| $\boldsymbol{i}_{o}$ | $\mathbf{1 6 . 6 8 \%}$ | $\mathbf{1 0 . 1 0 \%}$ |
| $\boldsymbol{i}_{i 1}$ | $\mathbf{6 4 . 9 8 \%}$ | $\mathbf{1 8 9 . 5 \%}$ |

## IV. CONCLUSIONS

This paper presents a complete comparison between two general control method for three-phase to three-phase AC/AC matrix converters. The first method is based on Least Mean Square Errors (LMSE) and the second method is based on novel PWM method.

Summery of some results obtained this research:

- Both algorithms ensure that the switches do not short-circuit the voltage sources, and do not opencircuit the current sources.
- Bout algorithms are capable of converting any input waveforms with any given angular frequency, to any output waveforms with any angular frequency.
- The peak value of the instantaneous output voltages for both algorithms is equal with the peak value of input voltages.
- In both algorithms the output voltages contain fundamental and additional high order harmonics.
- In both algorithms the output currents contain high order harmonics.
- Because of the load of the converters is almost always a low pass filter (R-L), therefore the output currents contain loss high order harmonics than the output voltages.
- In novel PWM method, the output voltages contain mainly $f_{o}$ and $f_{s}+\Delta f \mathrm{~Hz}$ harmonics, with $\Delta f$ being a function of $f_{i}$ and $f_{o}$. For increasing the frequency of harmonics, we should increase the frequency switching, and this needs the high-speed devices. Therefore the losses and cast will be high.
- In LMSE method, the output voltages contain mainly $f_{o}$ and $f_{o}+m \times f_{i} \mathrm{~Hz}$ harmonics ( $m$ is constant).
- The frequency of harmonics for $\boldsymbol{L M S E}$ algorithm is low, and near the main frequency. Therefore the design of the input and outputs filters will be complicated.
- The LMSE method is suitable for high output amplitude converting.
- The novel PWM method is suitable for lower output amplitude converting (see eqn. (13)).
- Simulation results show that the LMSE method is able to produce the undistorted and balanced output voltages, even if the input supply voltages are distorted and unbalanced [3], and this is not true for the second method.
- The THD for $\boldsymbol{L M S E}$ method is smaller than second method.
- The LMSE method has easy comprehension of the required commutation process.
- The LMSE method has simplified control algorithm.
- For LMSE method as the switches patterns show, because of the number of the switching are very low, therefore by this method the matrix converter has minimum loss.


## REFERENCES

[1] H. Altun, S. Sunter, "Simulation and Modeling of Vector Controlled 3-Phase Matrix Converter Induction Motor Drive", ELECO'01, Bursa, Nov. 7-11, 2001, pp. 98-102.
[2] Mehmet Ozdemir, Sedat Sunter, Baris Yurtseven, "Modeling And Simulation Of A Split Phase Induction Motor Fed By Single-Phase AC-AC Converter", International Journal For Engineering Modelling, Vol. 12, No. 1-4, 1999, pp. 17-23.
[3] S.H. Hosseini, E. Babaei, "A New Control Algorithm for Matrix Converters Under Distorted and Unbalanced Conditions", Proceedings of 2003 IEEE Conference on Control Application (CCA 2003), June 23-25, 2003, Istanbul, Turkey, Vol. 2, PP.1088-1093.
[4] S.H. Hosseini, E. Babaei, "A New Generalized Direct Matrix Converter", 2001 IEEE International Symposium on Industrial Electronics Proceedings (ISIE2001), Pusan, Korea, June 2001, vol. 2, pp. 1071-1076.
[5] S.H. Hosseini, E. Babaei, "A Novel Modulation Method for DC/AC Matrix Converters Under Distorted DC Supply Voltage" 2002 IEEE Region 10 Conference on Computers, Communications, Control and Power Engineering (IEEE TENCON'02), Beijing, China, October 2002, vol. 3, pp. 1970-1973.

